

DETAILED ACTION

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Attorney James A. Lucas on June 17, 2008. Discussed the possibility of including the mention of SRAM and DRAM as the memory devices to avoid confusion as to which memory device the control blocks are referring to. Attorney authorized and agreed on the change in claim 1, line 15.

As per currently amended claim 1, please insert the following amendment.

In claim 1, line 15, "said memory devices" has been replaced by --said SRAM and DRAM memory devices--.

2. The following is an examiner's statement of reasons for allowance:

The prior art fails to show the combination of the pipeline arrangement for memory access allocation of the network scheduler with a first SRAM containing a set of data buses and stores flow queue control blocks that are required for 'read' and 'write' at each flow queue enqueue and dequeue time, and a second SRAM containing two data buses, one dedicated to 'read' and one dedicated to 'write', and that stores frame control blocks and VLAN hierarchy control blocks and a first DRAM storing a network

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management counter and a second DRAM storing flow queue control blocks and VLAN hierarchy control blocks that require 'read' only at both enqueue time and dequeue time and the hierarchical structure of a physical port bandwidth that is divided into a plurality of logical links, the bandwidth available to each of the logical links being divided into a plurality of VLANs, and the bandwidth associated with each VLAN being shared by a plurality of individual user flows.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

/Abdullah Riyami/

Examiner, Art Unit 2616

/Huy D. Vu/

Supervisory Patent Examiner, Art Unit 2616